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Description: Third edition. New York, NY : McGraw-Hill, 2020.

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This book is based on the premise that starting with a high level programming language has its shortcomings. This premise lead to Patt/Patel's "bottom-up approach" found in "Introduction To Computing. " This text covers (in order) a switch level abstraction of a MOS Transistor, Logic Gates, latches, logic structures (MUX, Decoder, Adder, gated latches), finally culminating in an implementation of memory.

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that I wrote with Professor Sanjay Jeram Patel of Illinois. The book is intended as an introduction to computing for serious students of computing, including those in computer science, computer engineering, electrical engineering, or any other branch of engineering that involves systems that are controlled by an embedded computer.

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